

Grid Computing for the Bi-CGSTAB method Applied to Solution of the Finite-Element Hermite Collocation for Elliptic PDEs

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Key Words: *Collocation, Hermite Bi-Cubic Elements, 2-Cyclic Matrices, Preconditioning, Bi-CGSTAB, Grid Computing.*

ABSTRACT

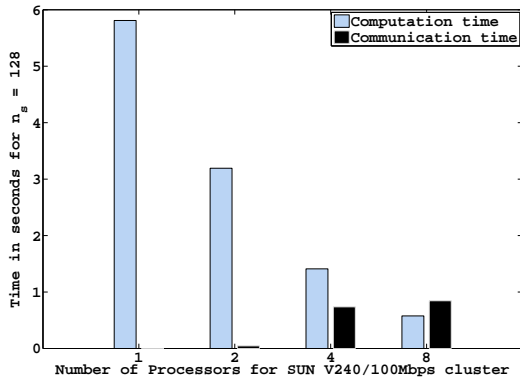
Hermite Collocation is a high order finite element scheme used as a PDE discretizer especially when continuous first derivatives are required. Among other properties, Collocation produces large and sparse systems of equations which poses no pleasant properties (e.g. symmetry). Memory requirements and performance are two of the main factors suggesting the usage of iterative methods on multiprocessor environments [1,2]. This motivated relevant research in the areas of iterative method analysis and parallel algorithm development. Main issues addressed were concerning both algorithmic (multi-color orderings, domain decomposition/partitioning techniques, parallel preconditioners, etc) and architectural (memory management/distribution, processor architecture, etc) aspects.

Working towards this direction, in [3], we have considered the implementation of the SOR and the Bi-CGSTAB [4] iterative methods for solving the Collocation system for the Poisson problem on Distributed-Shared memory (DSM) machines, improving the overall performance by managing the whole computation in order to maximize locality and minimize communication among the processing elements.

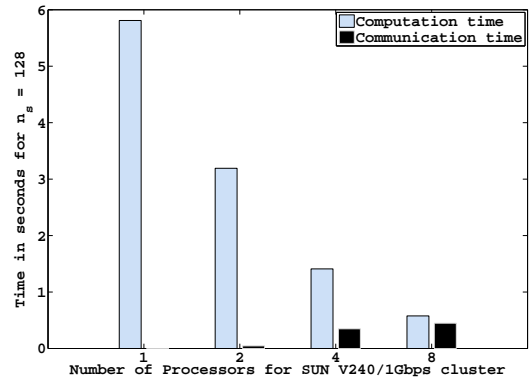
The work herein extends the results in [3] by addressing the problem of efficiently managing the intense *Grid computation* involved when the preconditioned Bi-CGSTAB Krylov method is employed for the iterative solution of the large and sparse linear system arising from the discretization of the *Modified Helmholtz-Dirichlet problem*

$$\begin{cases} \nabla^2 u(x, y) - \lambda u(x, y) = f(x, y), & (x, y) \in \Omega \\ u(x, y) = g(x, y), & (x, y) \in \partial\Omega \end{cases} \quad (1)$$

by the Hermite Collocation method. Taking advantage of the Collocation matrix's red-black ordered structure we organize efficiently the whole computation and map it on a pipeline architecture with master-slave communication. Its implementation, through MPI programming tools [5], is realized on a SUN V240 cluster, interconnected through a 100Mbps and 1Gbps ethernet network. To demonstrate its performance we have included results from the numerical experiments conducted, for several values of the parameter λ and discretization sizes n_s , accompanied by computation/communication and speedup measurements (see for example the Figures 1 and 2 that follow).

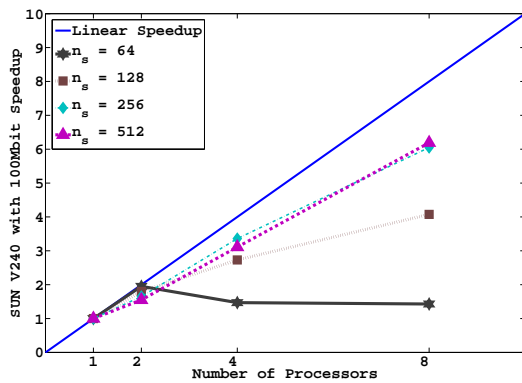


(a)

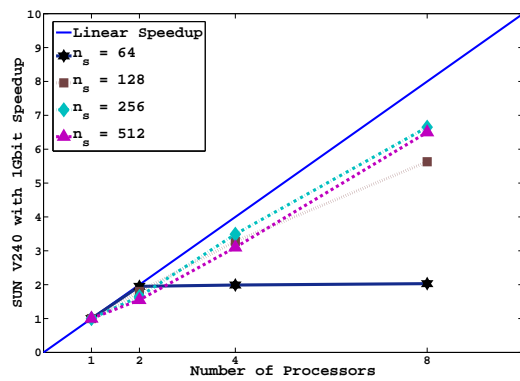


(b)

Fig. 1 : Time measurements for the SUN V240 cluster using (a) 100Mbps and (b) 1Gbps interconnection.



(a)



(b)

Fig. 2 : Speedup measurements for SUN V240 cluster using (a) 100Mbps and (b) 1Gbps interconnection.

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